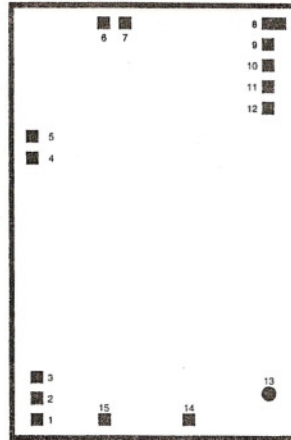




Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



HV91

Pin	Function	Pin	Function
1	COMP	9	-V _{BI}
2	Resol	10	NC
3	Shutdown	11	Output
4	V _{REF}	12	Sense
5	Discharge	13	+V _{BI}
6	OSC IN	14	BIAS
7	OSC OUT	15	FB
8	V _{DD}		

Topside Metal: Al

Backside: Si

Backside Potential:

Mask Ref:

Bond Pads : .004 min

APPROVED BY: CB

MFG: Supertex

DIE SIZE: .065" x .105"

THICKNESS: .019"

DATE: 2/6/01

P/N: HV9120Xd